Name:

|  |  |
| --- | --- |
| **D** | **CPU vocab** |
| **20** | Set of instructions required to make the computer start |
| **21** | The frequency which the CPU runs at, and the number of instructions which can be processed per second (measured in **22**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_) |
| **23** | Run the CPU at a higher clock speed than its default |

CPU and von Neumann Architecture

|  |  |  |  |
| --- | --- | --- | --- |
| **A** | **CPU structure** | | |
| **1** | | CU | **4**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ with the ALU, immediate access store and main memory to **5**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ the functions of the CPU. |
| **2** | |  | A collection of **6**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ with specific roles in the CPU |
| **3** | | ALU | Takes two **7**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ from the Accumulator and an **8**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ from the CIR and returns a single result to the Accumulator |

|  |  |  |
| --- | --- | --- |
| **B** | **Key vocab** | |
| **9** | | The way the components of a computer are arranged. |
| **10** | | System architecture where the data is stored in the same place as the instructions |
| **11** | | The cycle followed by the von Neumann architecture |

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **C** | **CPU hardware** | | | | |
| **12** | | | A connector which transfers data between components. Three types are data, address and control | | |
| **13** | | | Fast, expensive memory which is loaded from RAM and called by the CPU | | |
| **Clock generator** | | | A circuit which produces a **14**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ wave, which is the **15**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ frequency a CPU can perform instructions | | |
| **Core** | | | A processing unit which can run **16**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ with others. | | |
| **17** | | | | | Two cores |
| **Multi-core** | | | | **18** | |
| **Register** | | A section of **19**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ memory | | | |

Name:

|  |  |
| --- | --- |
| **D** | **CPU vocab** |
| **20** | Set of instructions required to make the computer start |
| **21** | The frequency which the CPU runs at, and the number of instructions which can be processed per second (measured in **22**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_) |
| **23** | Run the CPU at a higher clock speed than its default |

CPU and von Neumann Architecture

|  |  |  |  |
| --- | --- | --- | --- |
| **A** | **CPU structure** | | |
| **1** | | CU | **4**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ with the ALU, immediate access store and main memory to **5**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ the functions of the CPU. |
| **2** | |  | A collection of **6**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ with specific roles in the CPU |
| **3** | | ALU | Takes two **7**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ from the Accumulator and an **8**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ from the CIR and returns a single result to the Accumulator |

|  |  |  |
| --- | --- | --- |
| **B** | **Key vocab** | |
| **9** | | The way the components of a computer are arranged. |
| **10** | | System architecture where the data is stored in the same place as the instructions |
| **11** | | The cycle followed by the von Neumann architecture |

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **C** | **CPU hardware** | | | | |
| **12** | | | A connector which transfers data between components. Three types are data, address and control | | |
| **13** | | | Fast, expensive memory which is loaded from RAM and called by the CPU | | |
| **Clock generator** | | | A circuit which produces a **14**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ wave, which is the **15**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ frequency a CPU can perform instructions | | |
| **Core** | | | A processing unit which can run **16**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ with others. | | |
| **17** | | | | | Two cores |
| **Multi-core** | | | | **18** | |
| **Register** | | A section of **19**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ memory | | | |